NEW FACULTY IN HIGH PERFORMANCE COMPUTING (2016-2017)
Mohamed M. Sabry Aly joined the School of Computer Science and Engineering, Nanyang Technological University as an assistant professor in July 2017. Previously he was a postdoctoral research fellow at Stanford University and visiting scholar at EPFL, Switzerland.

He received his PhD from EPFL, Switzerland in 2013. His primary research interests are system-level design and optimization and resource management of computing systems by leveraging the benefits of emerging technologies (for computing, storage, interconnect, and cooling) to boost computing performance and energy efficiency beyond what is achievable today from large-scale cloud to small-scale embedded domains.

His recent work on NanoEngineered Computing Systems Technology (N3XT), under the supervision of Prof. Subhasish Mitra, has received worldwide recognition in various news press. It shows the prospects of monolithic 3D computing systems enabled by emerging nano technologies, such as carbon nanotubes and resistive memories.

He was the recipient of the Swiss National Science Foundation Early Post-Doctoral Mobility Fellowship in 2013.
As an outstanding young researcher in embedded systems, Dr. Weichen Liu joined School of Computer Science and Engineering, Nanyang Technological University, Singapore, in August 2017. Before that, he was a faculty member in Chongqing University, China, a research fellow in Nanyang Technological University, and a visiting scholar in the Hong Kong University of Science and Technology, Beijing University of Post and Telecommunications, and National Tsing Hua University, respectively.

Dr. Liu received his PhD degree in computer science and engineering from the Hong Kong University of Science and Technology, and the MEng and BEng degrees from Harbin Institute of Technology, China. His research interests include embedded and real-time systems, many-core systems, reliable and fault-tolerant systems, and electronic/photonic network-on-chip. His recent research mainly focuses on scalable on-chip communication architectures.

Dr. Liu served as the chairs, technical program committee members, organizing committee members, and associate editors for over 20 premier international conferences and journals, including RTAS 2017, ICCAD 2017, CODES+ISSS 2016-2017, ASP-DAC 2016-2017, etc. He authored and co-authored more than 70 research papers in peer-reviewed journals, conferences and books, and received the best paper candidate awards from ASP-DAC 2016, CASES 2015, CODES+ISSS 2009, the best poster award from RTCSA 2017 and AMD-TFE 2010, and the most popular poster award from ASP-DAC 2017. His research is supported globally by more than 11 government funded competitive grants from Singapore, Hong Kong and China, and more than 6 collaborative projects with the leading industrial partners including Intel, AMD, Xilinx, MediaTek and Huawei.